

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	43345	MOS ADJ transistor	USPAT; US-PGP UB; EPO; JPO	2002/04/24 17:03
2	BRS	L2	98	1 and (plasma NEAR damage)	USPAT; US-PGP UB; EPO; JPO	2002/04/24 17:31
3	BRS	L3	552	1 and (bypass same circuit)	USPAT; US-PGP UB; EPO; JPO	2002/04/24 17:32
4	BRS	L4	436	3 and (metal conduct\$3)	USPAT; US-PGP UB; EPO; JPO	2002/04/24 17:33
5	BRS	L5	197	4 and (dielectric insulat\$)	USPAT; US-PGP UB; EPO; JPO	2002/04/24 17:34
6	BRS	L6	197	4 and (dielectric insulat\$3)	USPAT; US-PGP UB; EPO; JPO	2002/04/24 17:34
7	BRS	L7	5	6 and fusion	USPAT; US-PGP UB; EPO; JPO	2002/04/24 17:35
8	BRS	L8	180	6 and well	USPAT; US-PGP UB; EPO; JPO	2002/04/24 17:36
9	BRS	L9	128	8 and (STI FOX oxide)	USPAT; US-PGP UB; EPO; JPO	2002/04/24 17:36

	Comments	Error Definition	Errors
1			0
2			0
3			0
4			0
5		Truncation overflow.	1
6			0
7			0
8			0
9			0